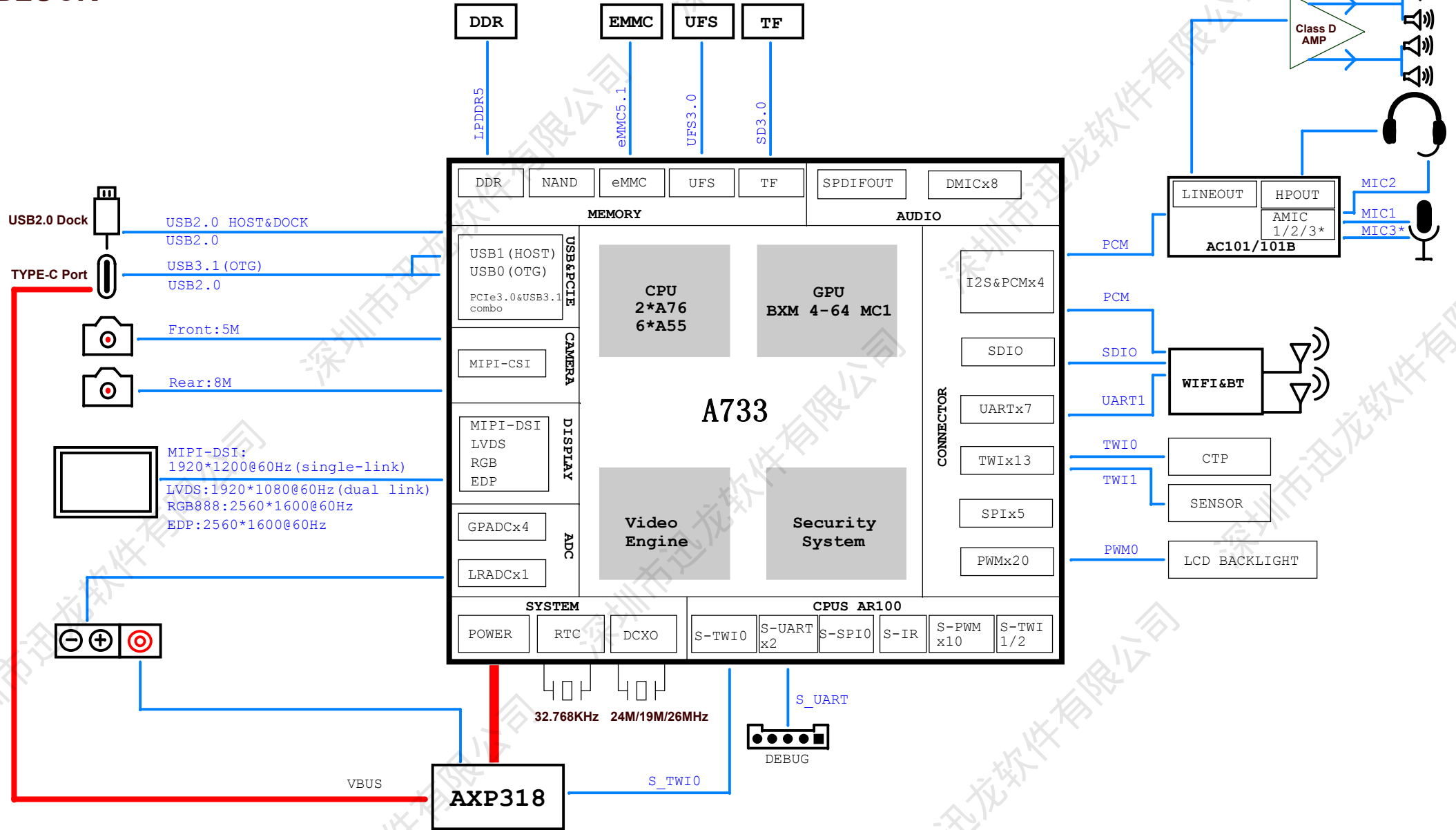
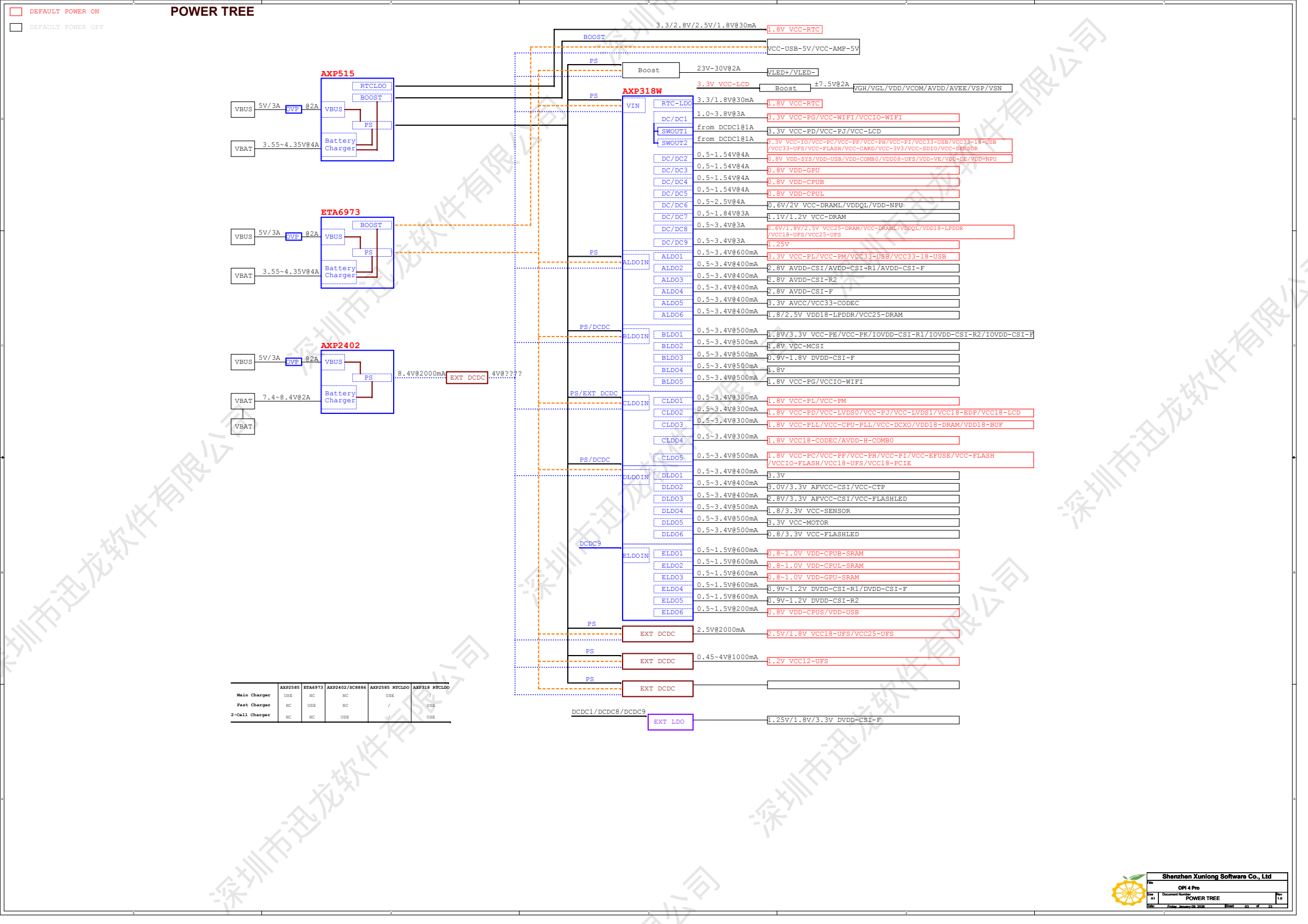


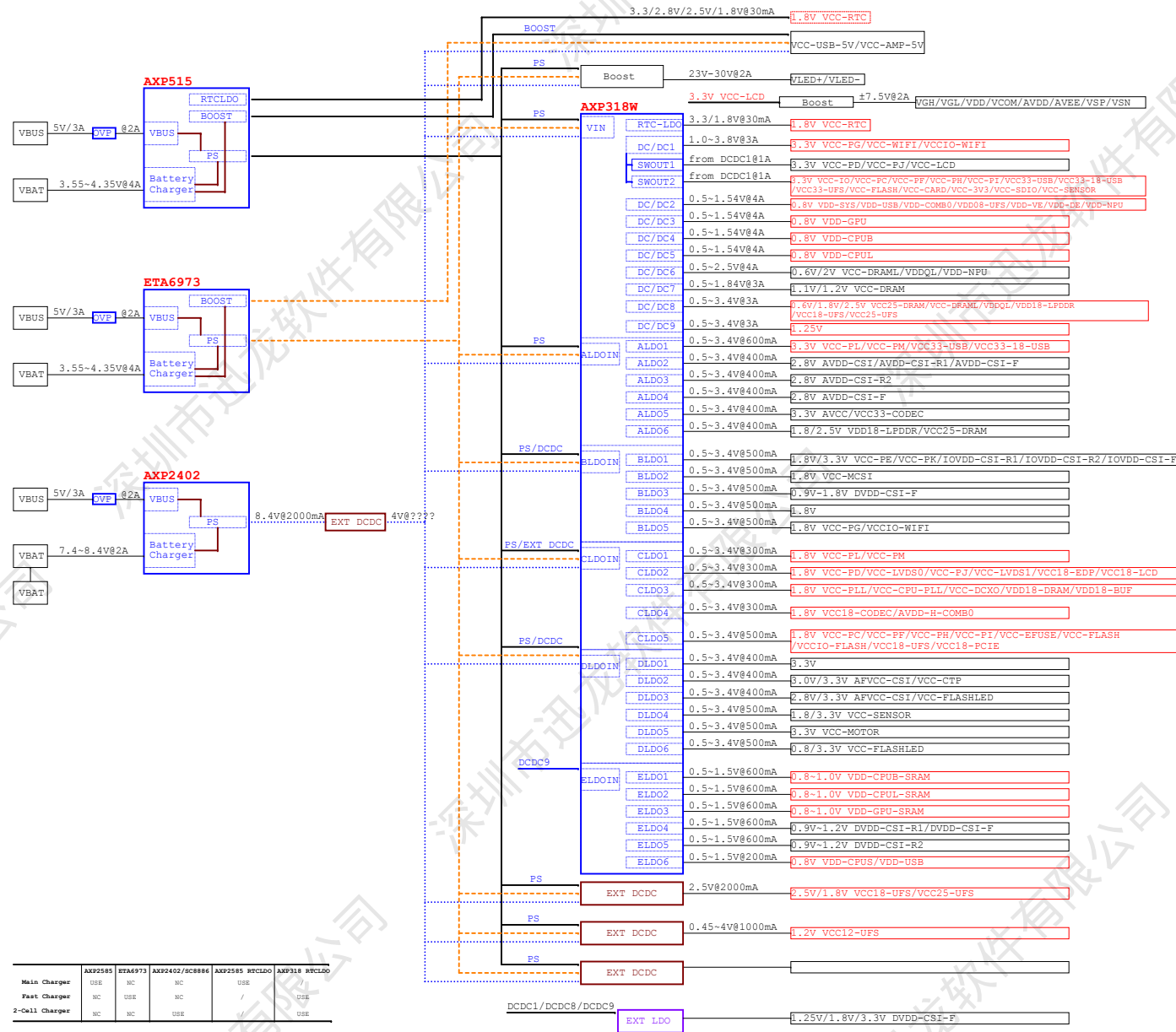
# BLOCK





DEFAULT POWER ON  
 DEFAULT POWER OFF

**POWER TREE**

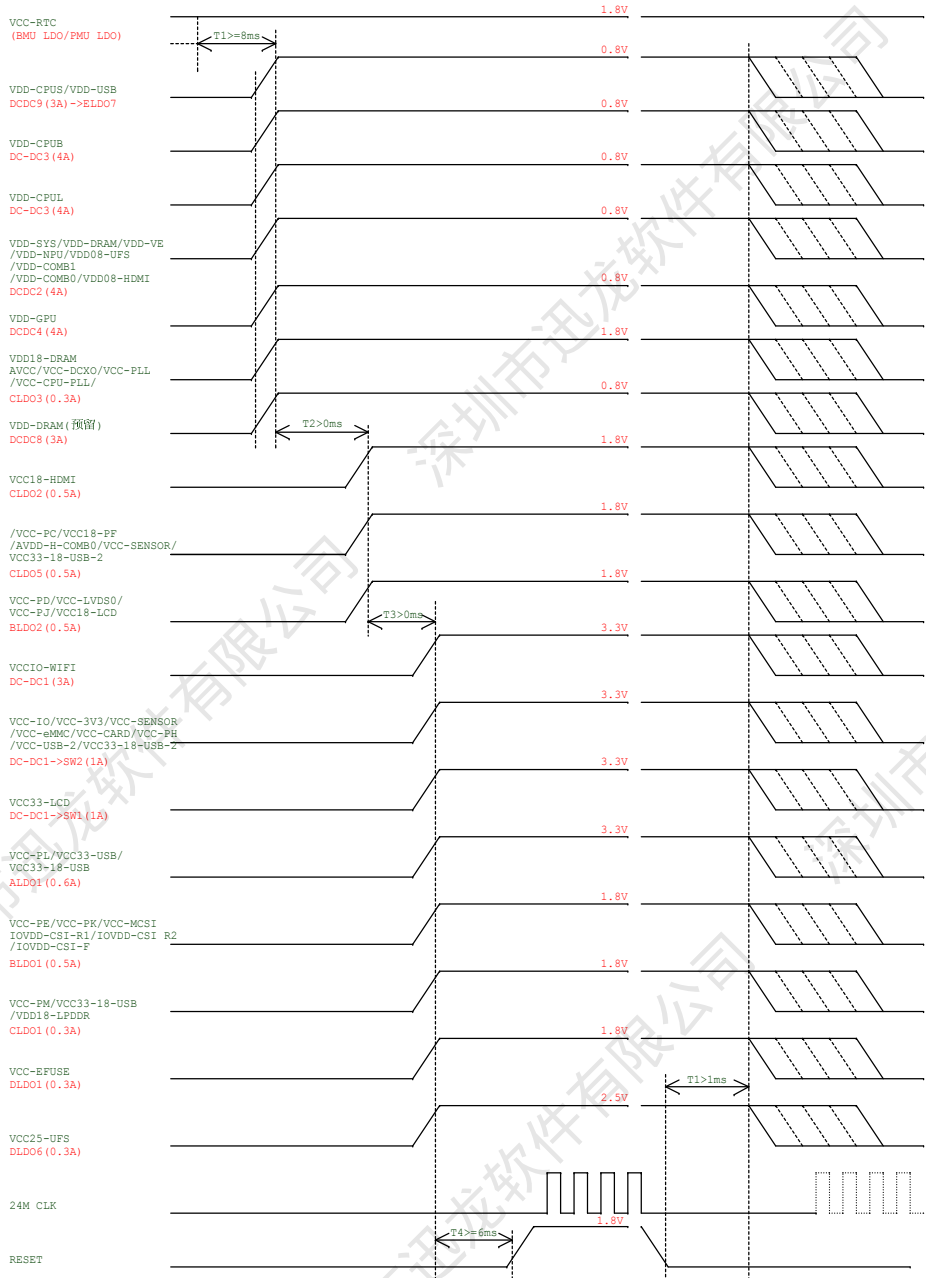


Main Charger	AXP2585	ETA6973	AXP2402/SC8886	AXP2585 RTCLDO	AXP318 RTCLDO
USE	NC	NC	NC	USE	USE
Fast Charger	NC	USE	NC	/	USE
2-Cell Charger	NC	NC	USE	/	USE

# POWER SEQUENCE

## POWER ON SEQUENCE

## POWER OFF SEQUENCE

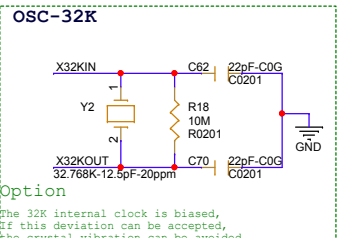
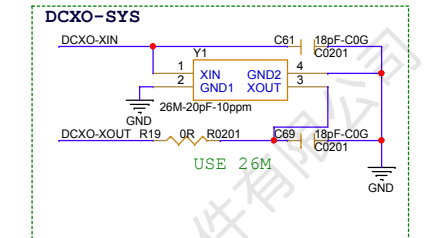
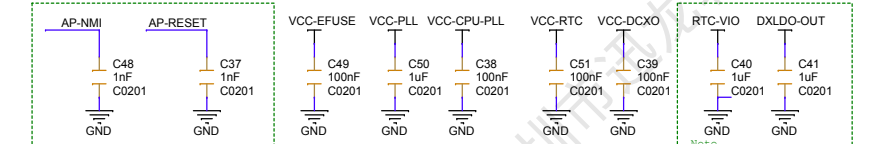
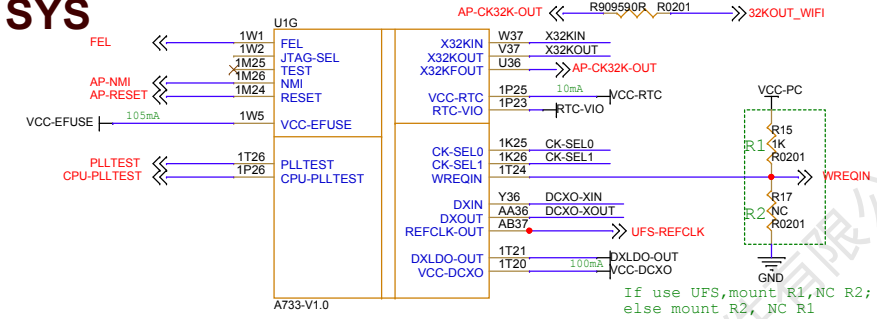


Shenzhen Xunlong Software Co., Ltd		
Title	A733	
Size	Document Number	Rev
A3	POWER SEQUENCE	1.0
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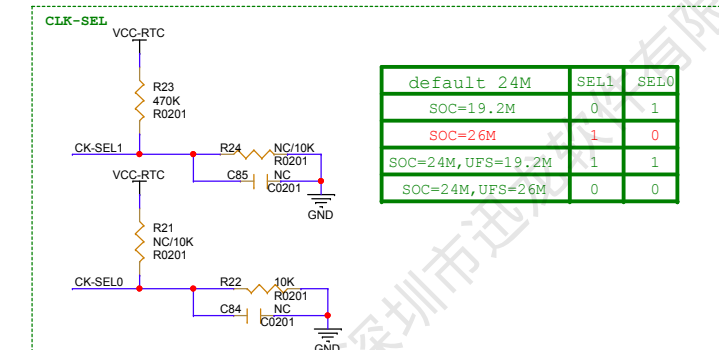
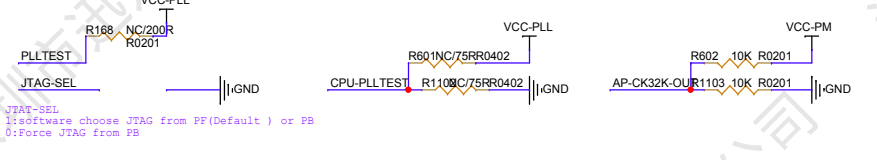




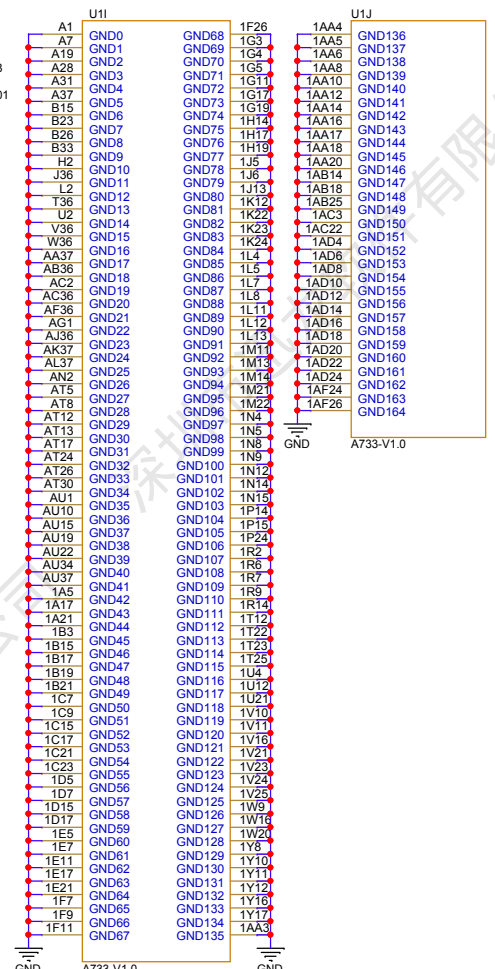
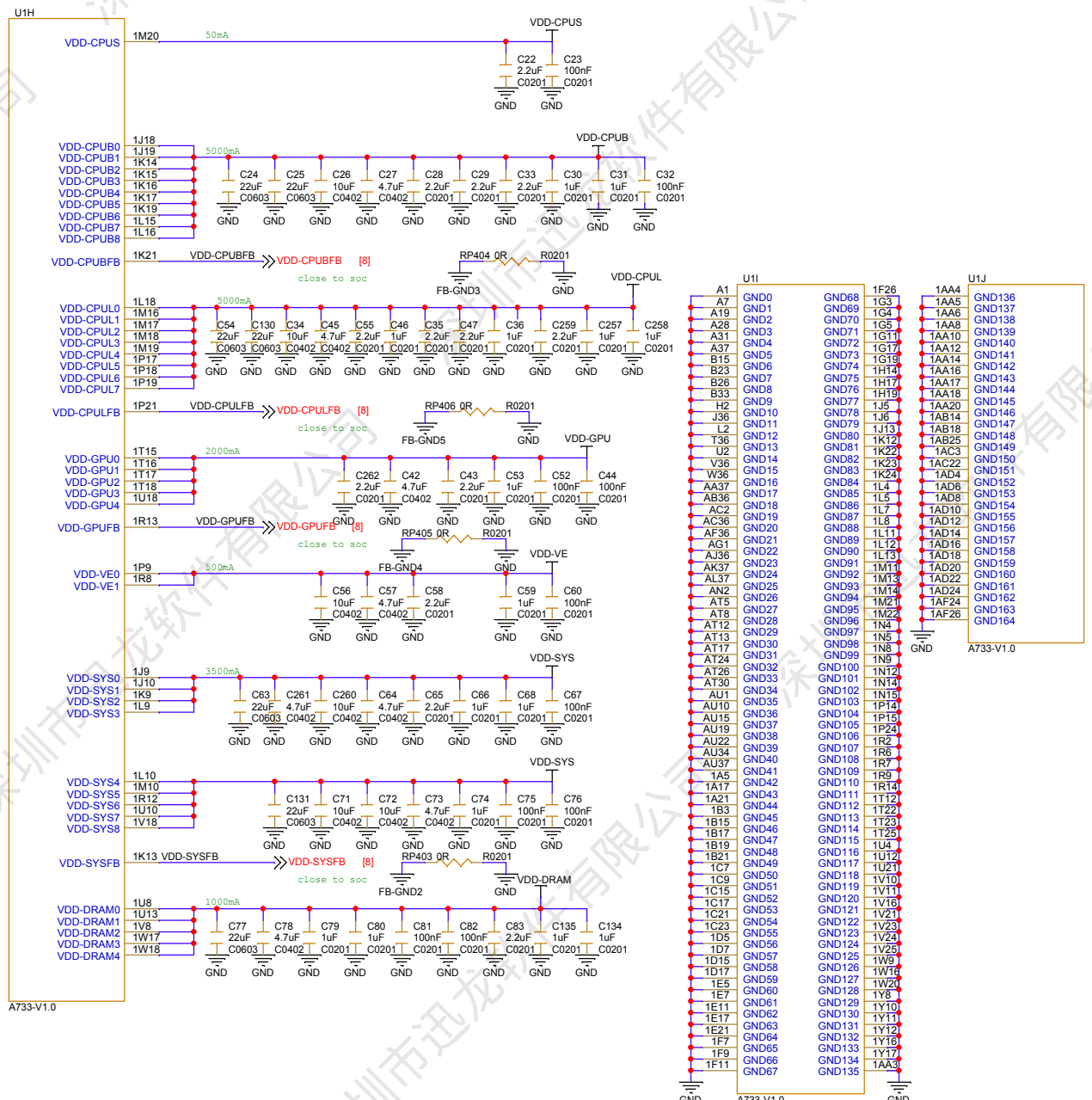
# SYS



# SYS-TEST



# CORE POWER



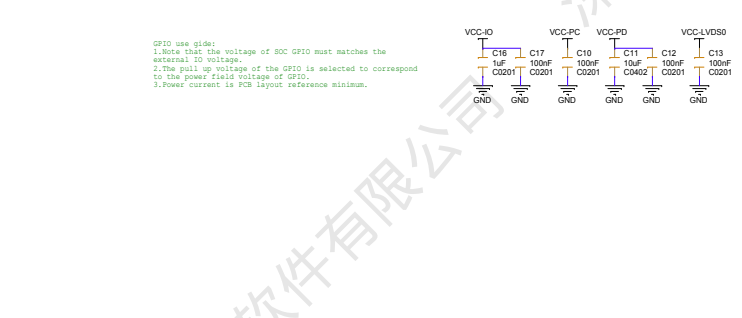
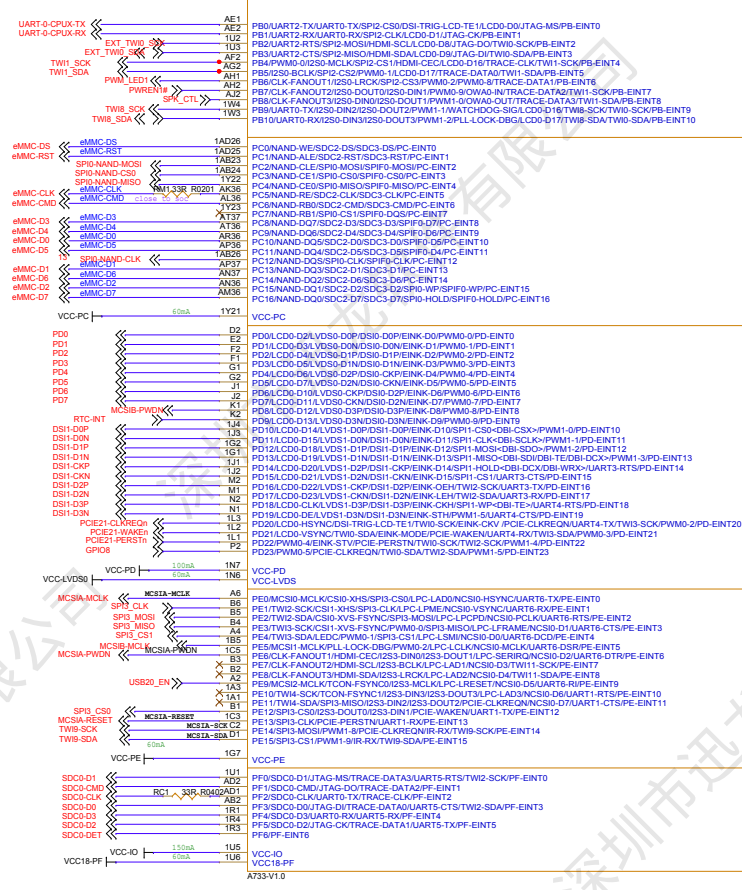
**Shenzhen Xunlong Software Co., Ltd**

Title: **OPI 4 Pro**

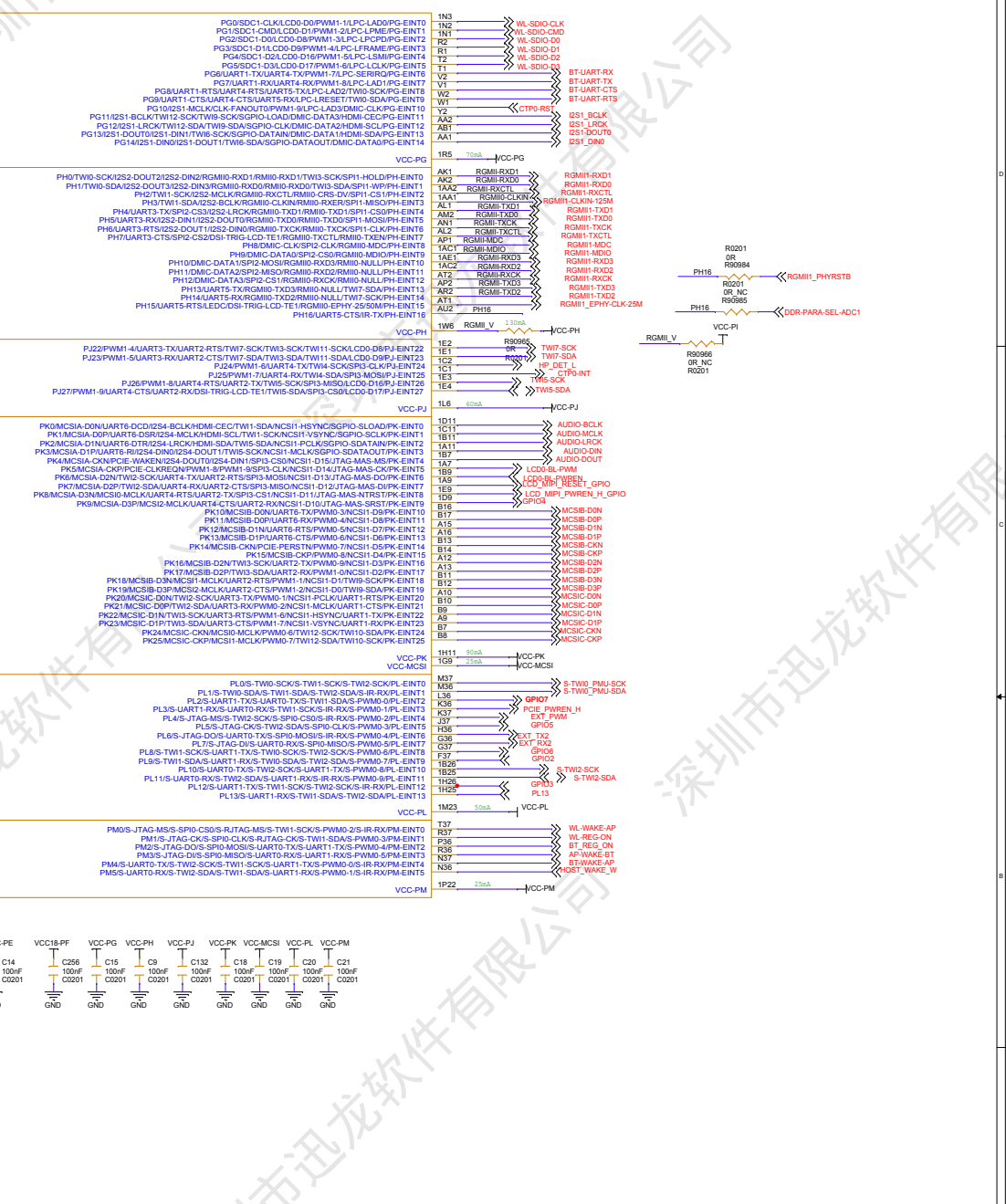
Size: A3	Document Number: <b>A733 SYS</b>	Rev: 1.0
Date: Friday, January 09, 2026	Sheet: 07	of 23

Note D0: The line sequence of LVDS and MIFI-D01 is inconsistent, as shown in the following table.

Table with 4 columns: GPIO, LVDS Function, MIFI-D01 Function. Lists various GPIO pins and their corresponding LVDS and MIFI-D01 functions.



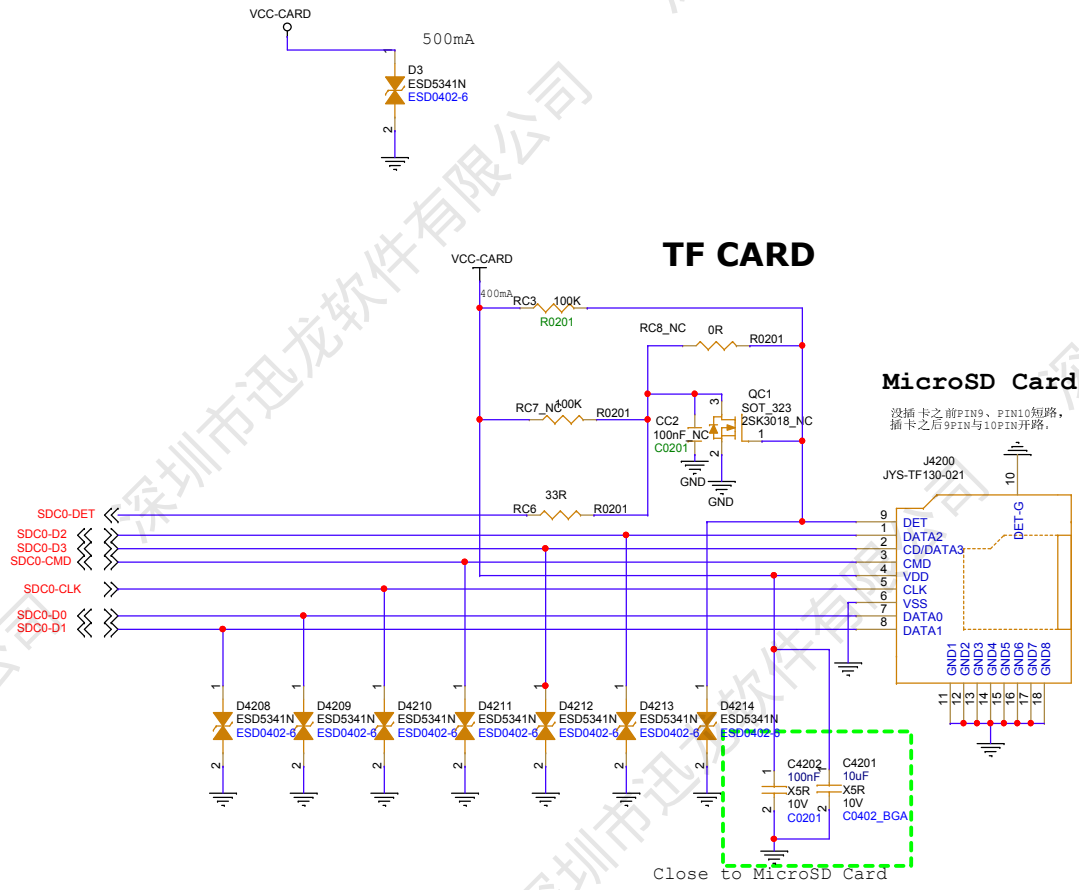
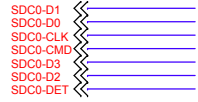
GPIO use guide: 1. Note that the voltage of SOC GPIO must matches the external IO voltage. 2. The pull up voltage of the GPIO is selected to correspond to the power field voltage of GPIO. 3. Power current is PCB layout reference minimum.



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# TF CARD



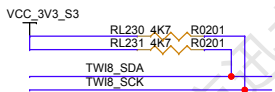
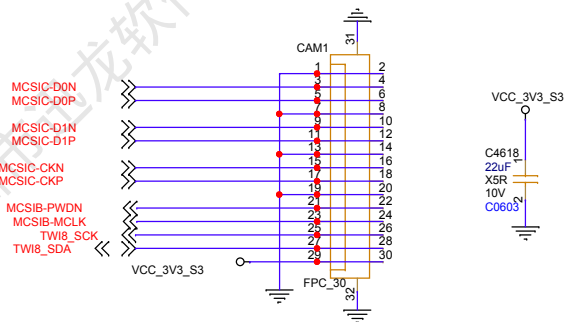
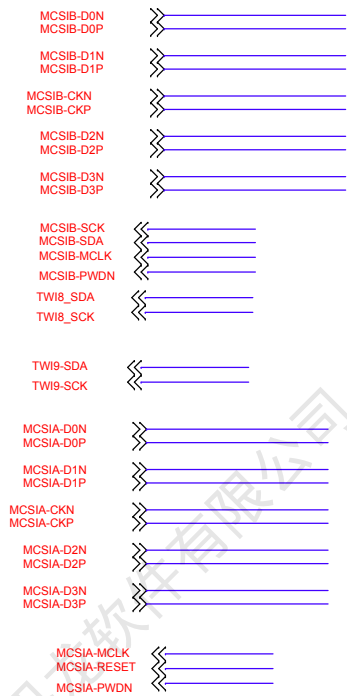
## MicroSD Card

没插卡之前PIN9、PIN10短路,  
插卡之后9PIN与10PIN开路。

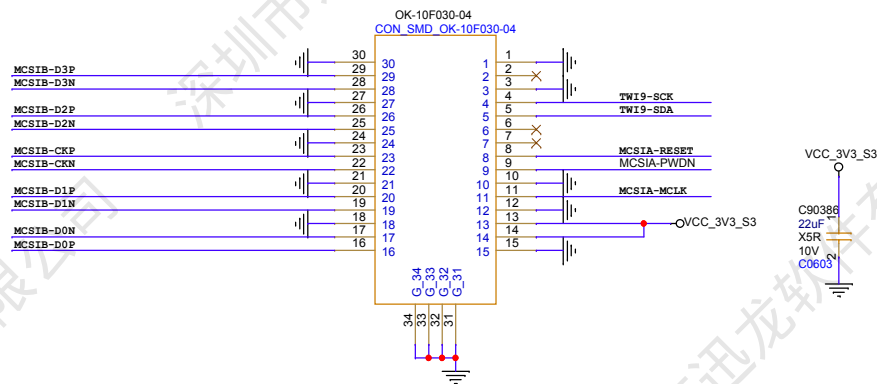


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A3	TF Card	1.0
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# VI-Camera DPHY\_RX

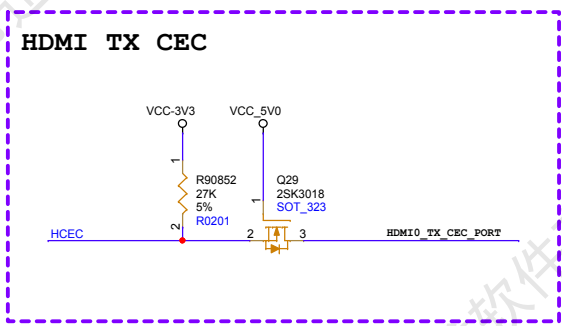
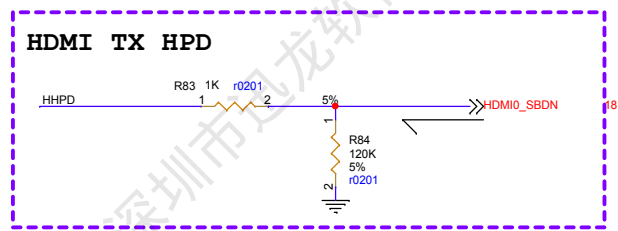
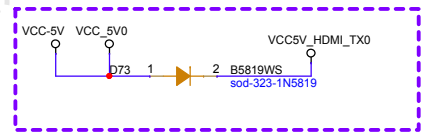
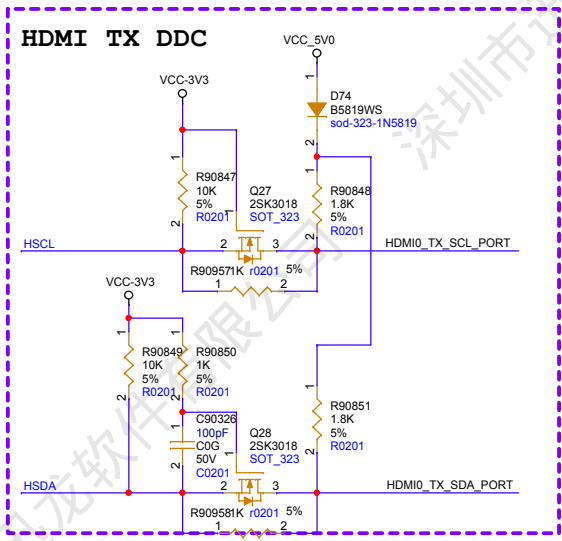
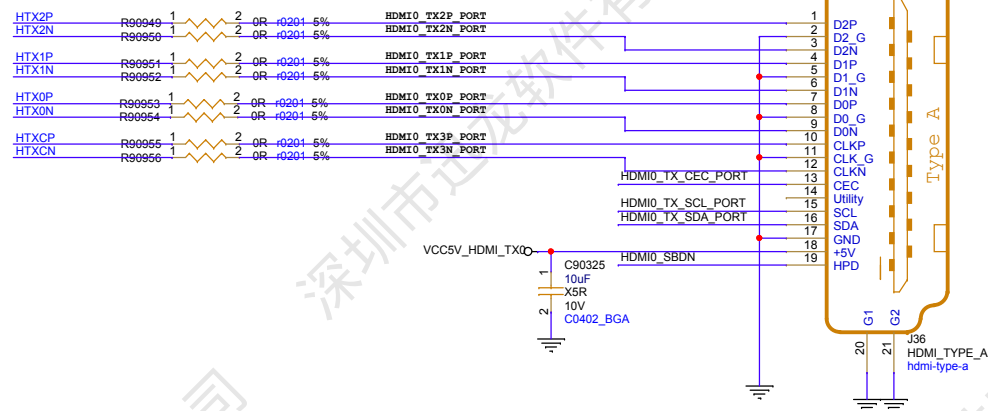
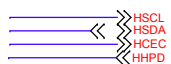


# MIPI D-PHY1 CAM2

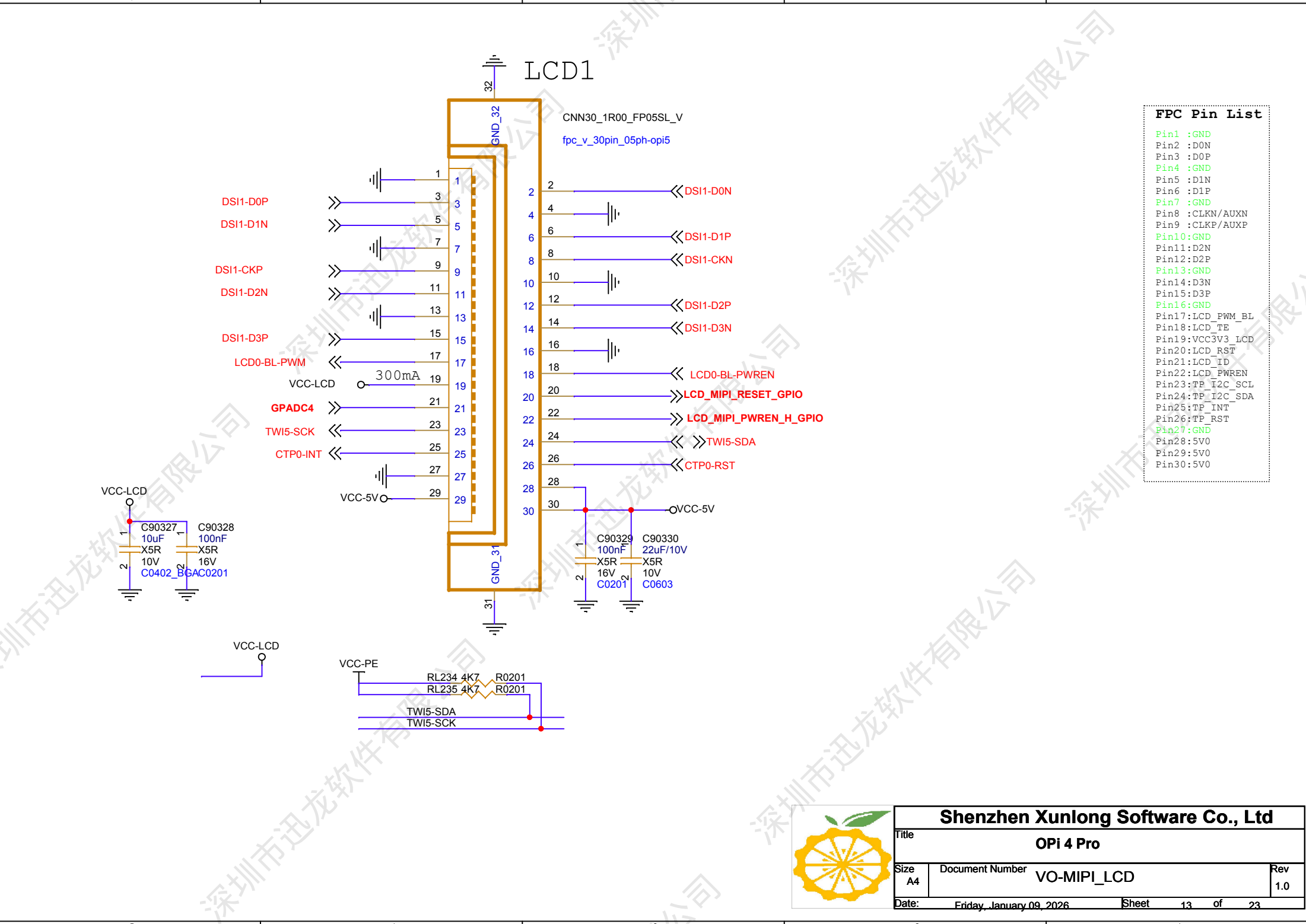


Shenzhen Xunlong Software Co., Ltd			
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Size	Document Number	VI-Cam MIPI CSIA-RX	
A3		Rev	1.0
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# HDMI



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# LCD1

CNN30\_1R00\_FP05SL\_V  
fpc\_v\_30pin\_05ph-opi5

### FPC Pin List

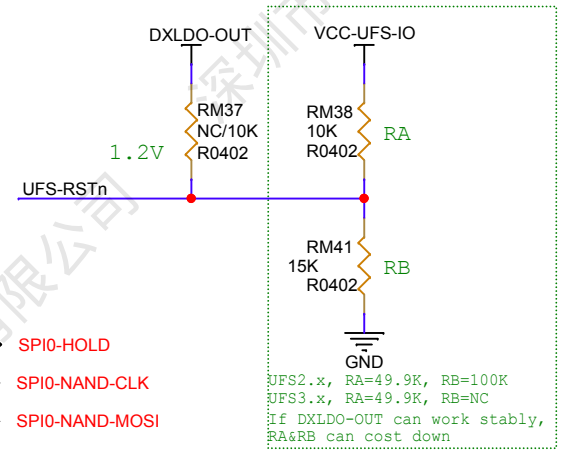
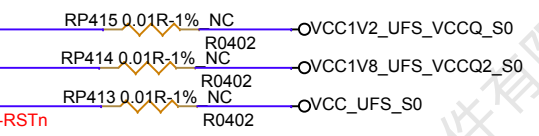
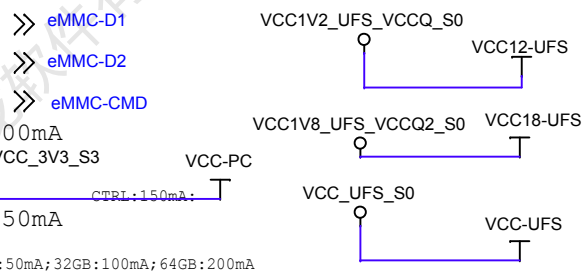
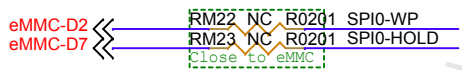
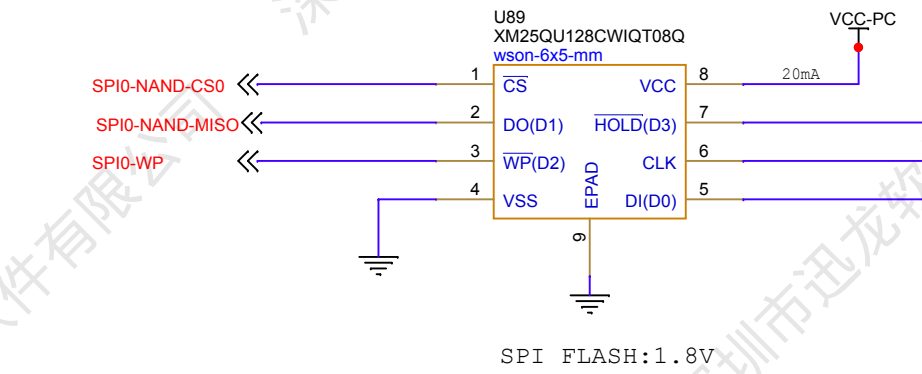
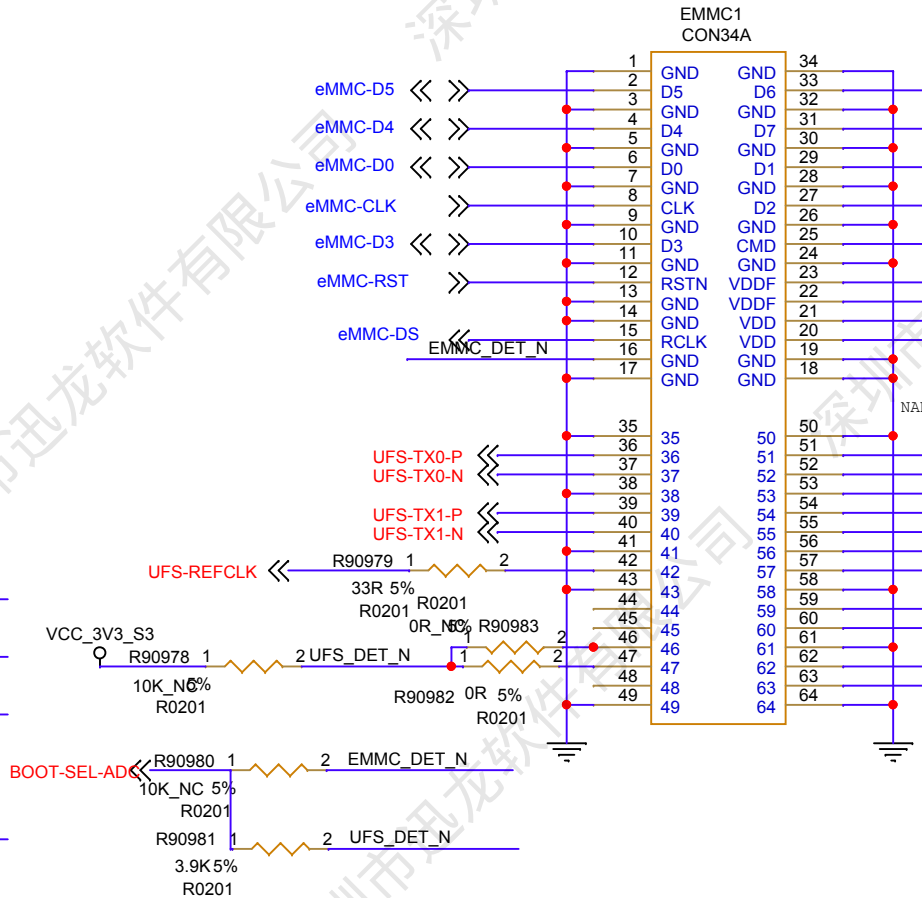
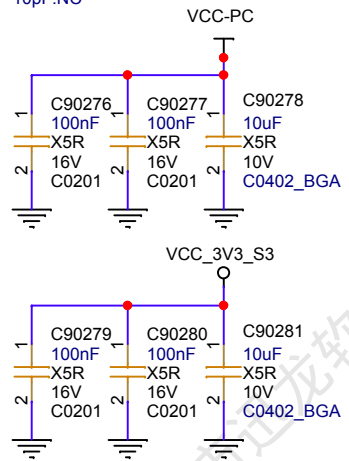
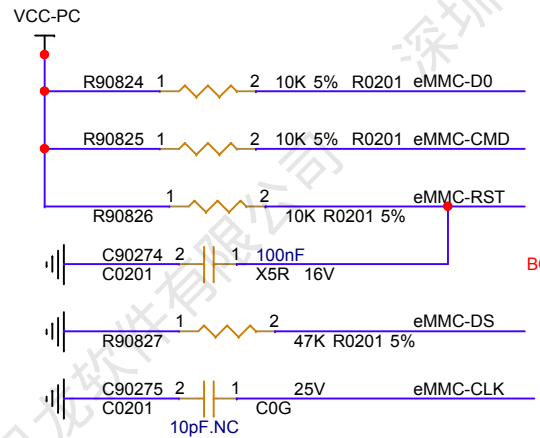
- Pin1 :GND
- Pin2 :D0N
- Pin3 :D0P
- Pin4 :GND
- Pin5 :D1N
- Pin6 :D1P
- Pin7 :GND
- Pin8 :CLKN/AUXN
- Pin9 :CLKP/AUXP
- Pin10:GND
- Pin11:D2N
- Pin12:D2P
- Pin13:GND
- Pin14:D3N
- Pin15:D3P
- Pin16:GND
- Pin17:LCD\_PWM\_BL
- Pin18:LCD\_TE
- Pin19:VCC3V3\_LCD
- Pin20:LCD\_RST
- Pin21:LCD\_ID
- Pin22:LCD\_PWREN
- Pin23:TP\_I2C\_SCL
- Pin24:TP\_I2C\_SDA
- Pin25:TP\_INT
- Pin26:TP\_RST
- Pin27:GND
- Pin28:5V0
- Pin29:5V0
- Pin30:5V0



<b>Shenzhen Xunlong Software Co., Ltd</b>		
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# eMMC FLASH

- eMMC-D0 << >>
- eMMC-D1 << >>
- eMMC-D2 << >>
- eMMC-D3 << >>
- eMMC-D4 << >>
- eMMC-D5 << >>
- eMMC-D6 << >>
- eMMC-D7 << >>
- eMMC-DS << >>
- eMMC-RST << >>
- eMMC-CLK << >>
- eMMC-CMD << >>



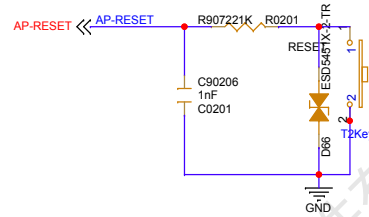
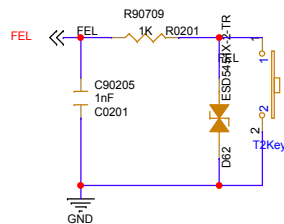
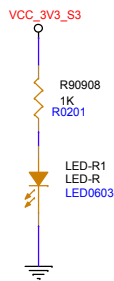
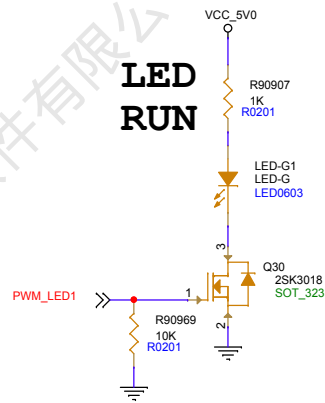
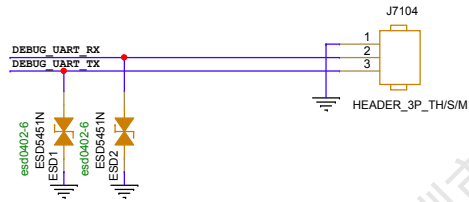
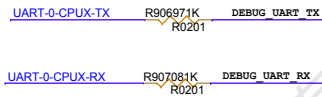
<b>Xunlong Co., Limited</b>		
Design Name		
<b>OPI 4 Pro</b>		
Size	Page Name	Rev
A4	Flash-eMMC Flash	1.0
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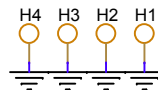
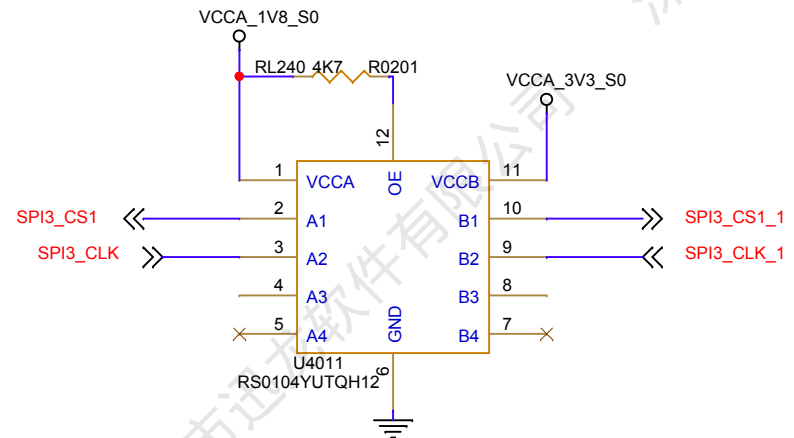
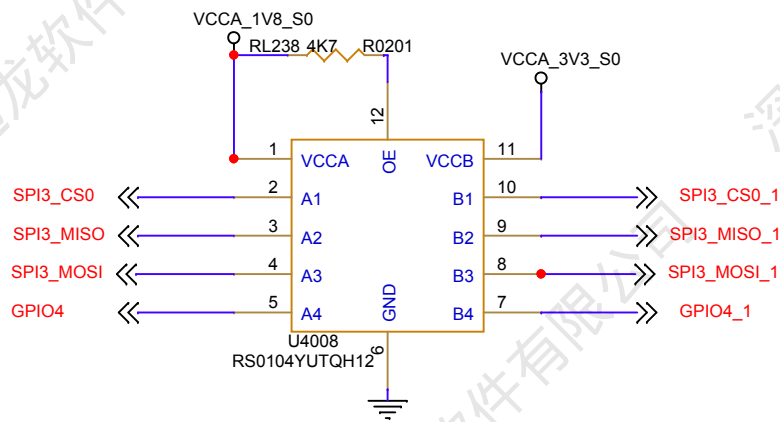
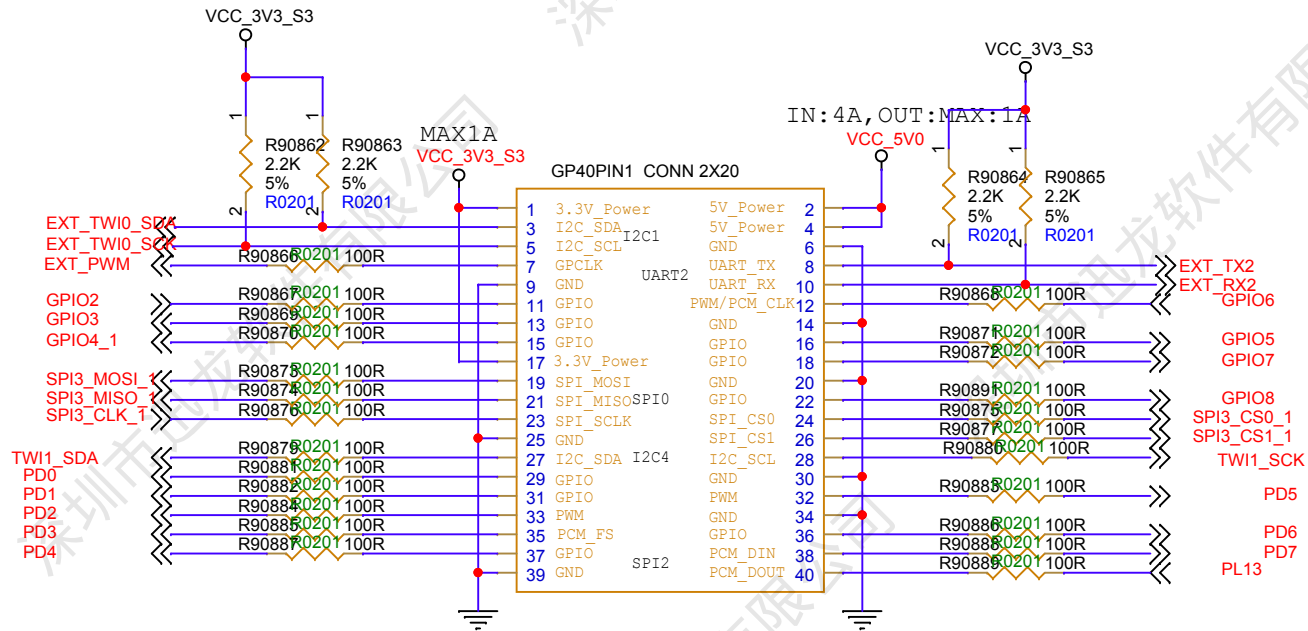
# CPUX DEBUG

UART-0-CPUX-TX  
 UART-0-CPUX-RX

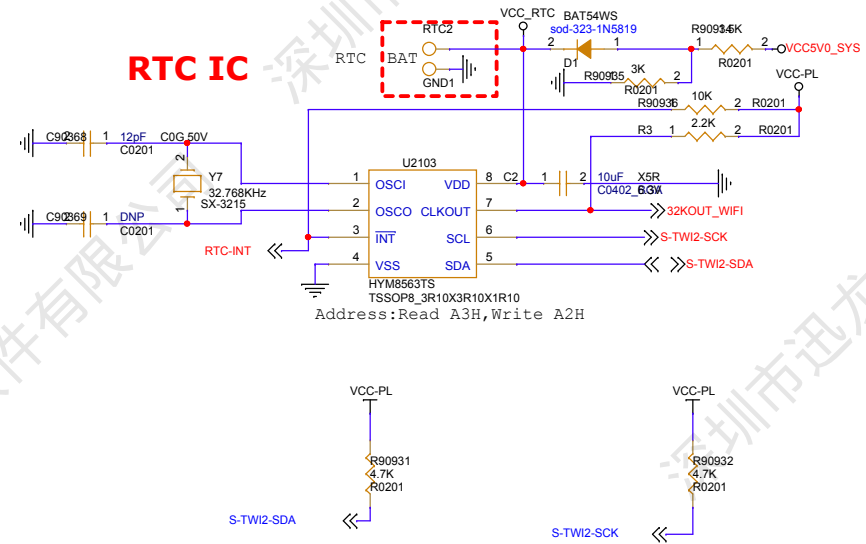
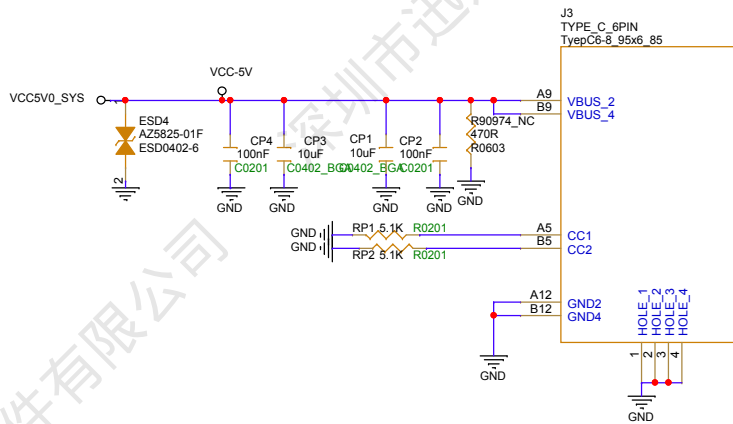


Shenzhen Xunlong Software Co., Ltd		
Title	OPI 4 Pro	
Size	Document Number	Rev
A3	DEBUG/TEST	1.0
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# EXT I/O

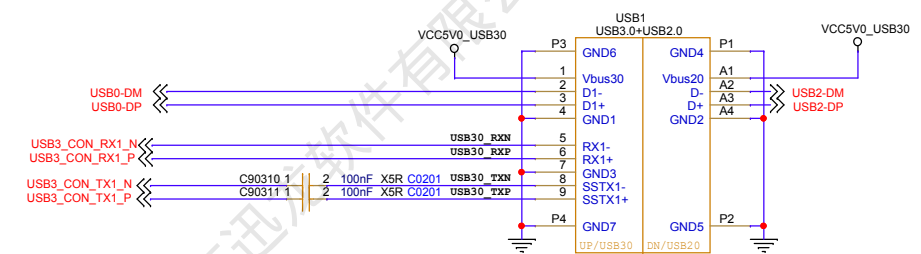
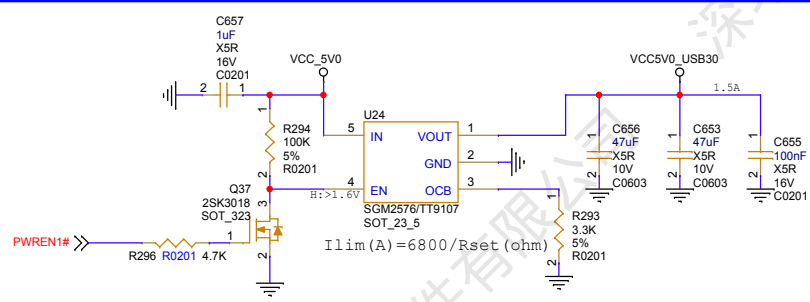
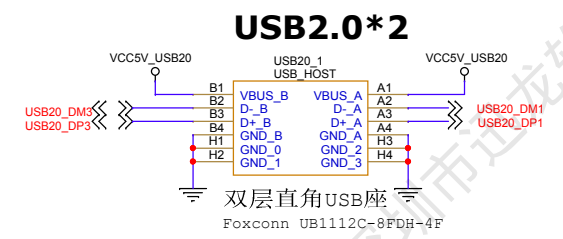
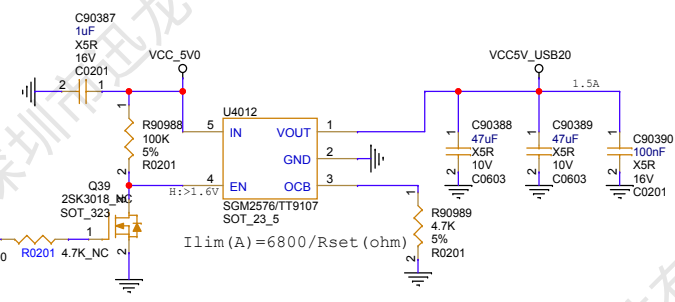
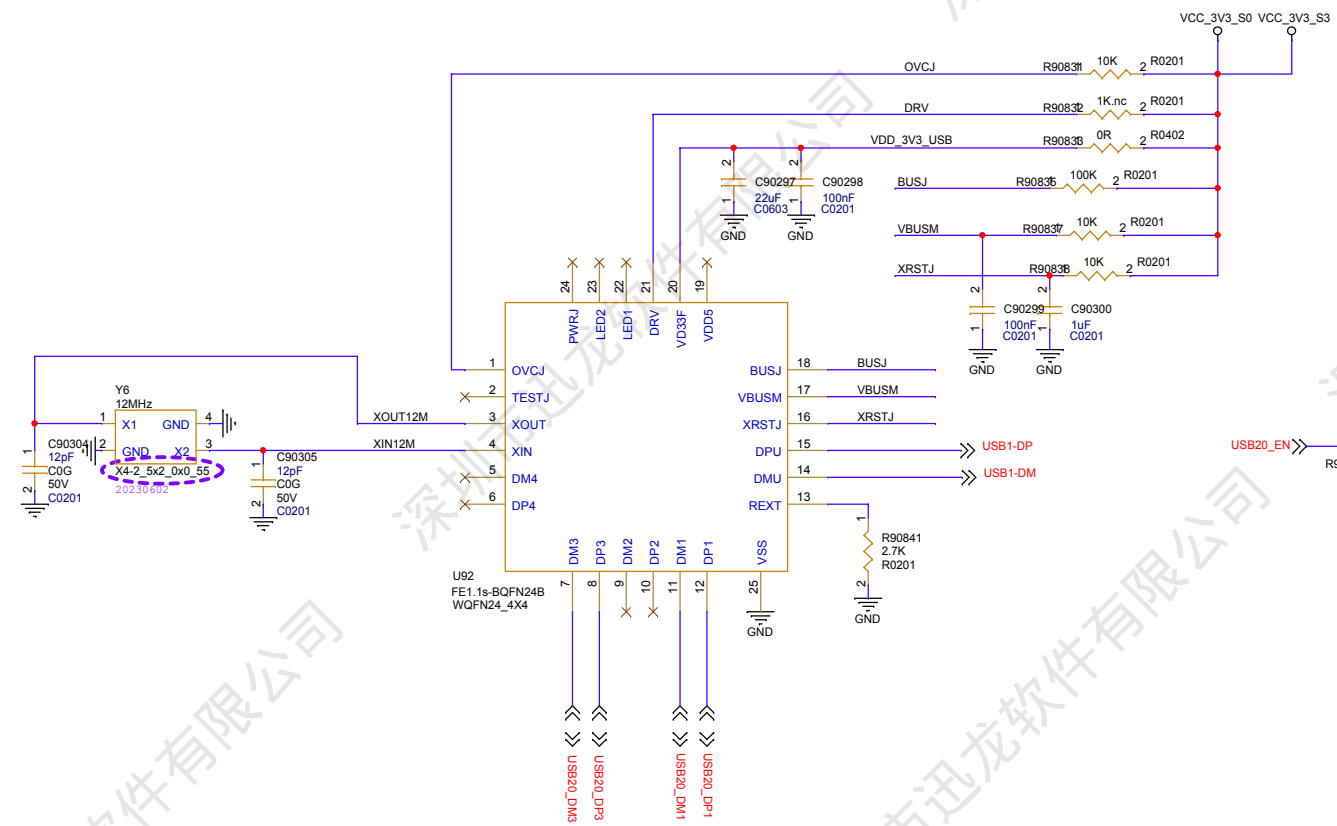


<b>Shenzhen Xunlong Software Co., Ltd</b>		
Title		
<b>OPI 4 Pro</b>		
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A4	<b>EX_GPIO</b>	1.0
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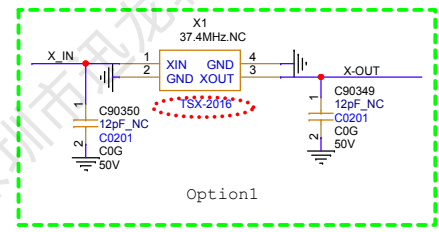
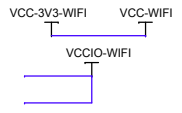


<b>Shenzhen Xunlong Software Co., Ltd</b>			
<b>OPI 4 Pro</b>			
Size	Document Number	Rev	
A3	Power_Type-C	1.0	
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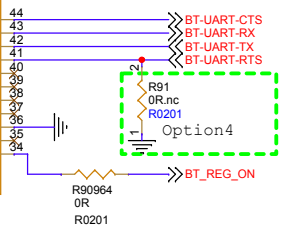
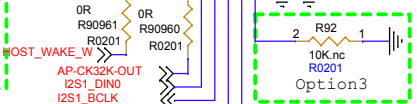
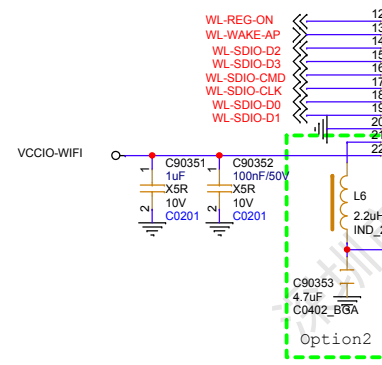
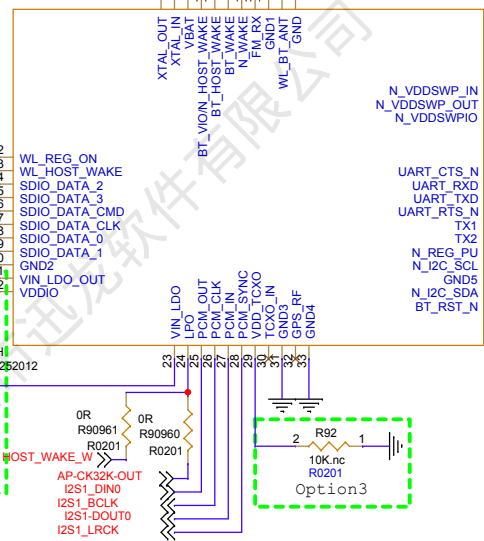
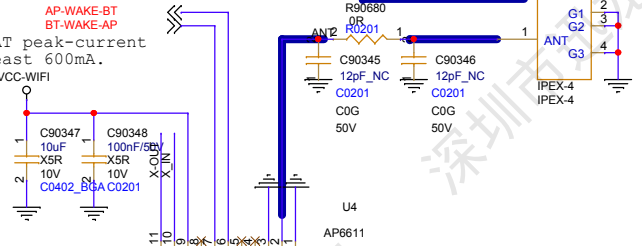




<b>Shenzhen Xunlong Software Co., Ltd</b>			
<b>OPI 4 Pro</b>			
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A3	USB20/USB30 HOST Port	1.0	
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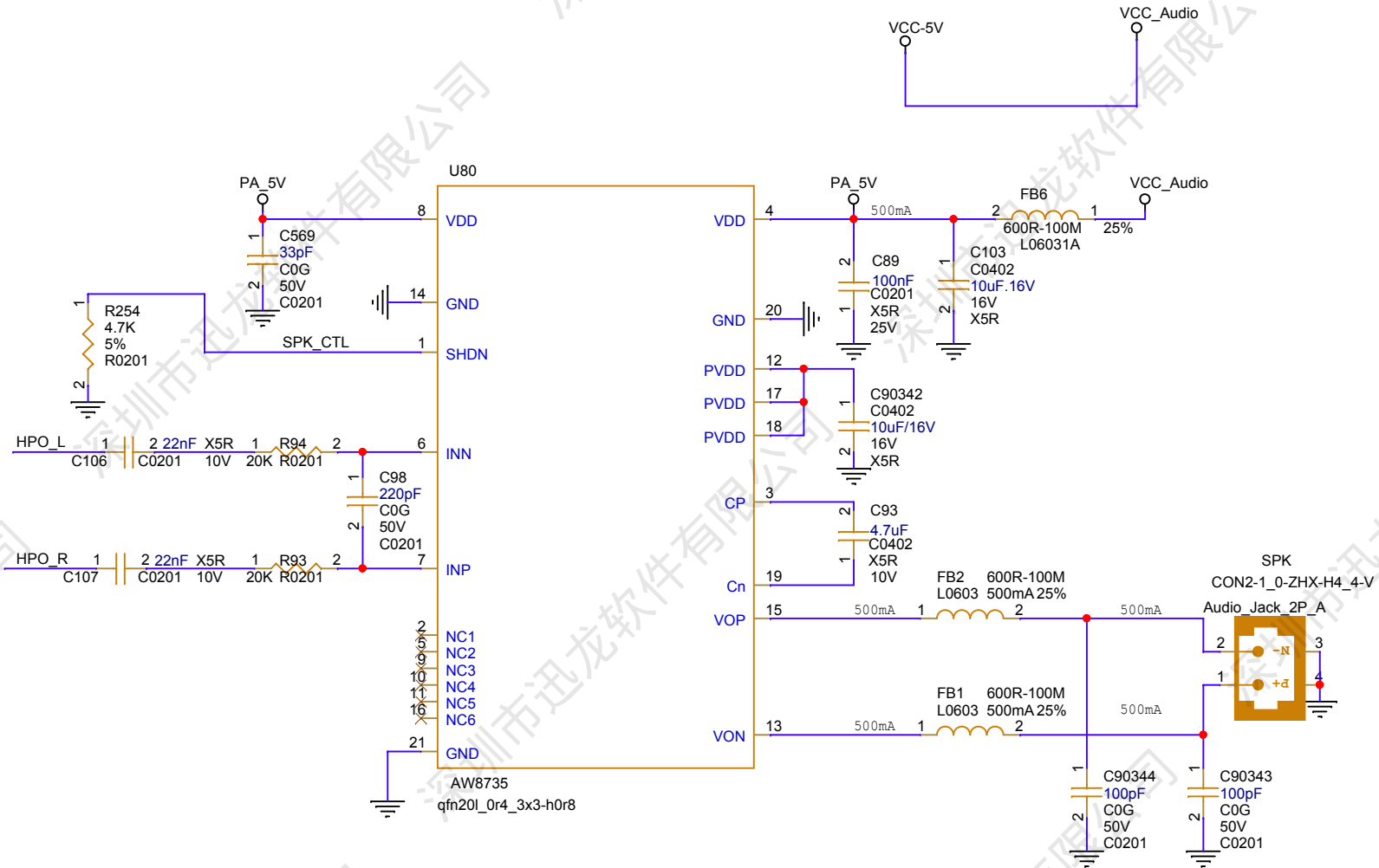
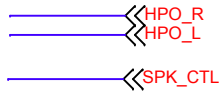


Note: VBAT peak-current is at least 600mA.



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A3	WIFI/BT-SDIO	1.0
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# SPK PA



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<b>Size</b>	<b>Document Number</b>	<b>Rev</b>
A4	SPK_PA	1.0
<b>Date:</b>	Friday, January 09, 2026	<b>Sheet</b> 23 <b>of</b> 23